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	Applicant: Shoubhik Mukhopadhyay	
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## U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	2004/0005017 A1	Jan. 8, 2004	Borowski et al.			
	2003/0222820	Dec. 4, 2003	Karr et al.			
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	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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	Y. Zhao and S. Dey, "Separate Dual Transistor Register-an Circuit Solution for on-line Testing of Transient Errors in UDSM-IC," in Proceedings of Intl. On-Line Testing Symposium 2003, Kos Island, Greece, 2003.
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\*Examiner: Initial if citation considered whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.